RSVR-78Mv3

0.5A Non-Isolated, regulated DC/DC Converter





3 Pin SIL Package

■ Wide Input Range

Step-down switching

■ Full SMD Technology

■ Efficiency up to 95%

■ Operating Temperature Range -40°C ~ +85°C

■ Continuous Short Circuit Protection (self-recovery)

Pin-out compatible with LM78MXX three terminals positive Regulator

■ Non Conductive Black Plastic Case

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Output Specifications	
Voltage Accuracy	±2% typ., ±3 ~ 4% max.
Output Voltage Adjustability (Trim)	-
Maximum Output Current	500mA or -300mA max.
Line Regulation	±0.2% typ., ±0.4% max.
Load Regulation	from 10% to 100% Load: ±0.3 ~ 0.6% typ. –
Short Circuit Protection	Continuous, self-recovery
Ripple & Noise (20 MHz bandwidth)	20mV typ., 75mV pk-pk max.
Temperature Coefficient	±0.03%/°C
Transient Recovery Time	200μs typ., 1ms max.
Transient Response Deviation	50mV typ., 250mV max.

Input Specifications	
Voltage Range	See table
Start-up Time	_
No-Load/Full-Load Input Current	See table
Input Filter	-
Input Reflected Ripple Current	_
Remote ON	_
Remote OFF	_
OFF Idle Current	-
Surge Voltage (100 ms) †)	

General Specifications	
Switching Frequency	550kHz ~ 850kHz max.
Humidity	95% rel H
Reliability Calculated MTBF	>2.0MHrs (MIL-HDBK-217 f)
Safety Standard(s)	IEC/EN60950-1 (designed to meet)

Environmental Specifications	
Operating Temperature Range	-40°C ~ +85°C (see Derating Curve)
Maximum Case Temperature	-
Storage Temperature	-55°C ~ +125°C
Cooling	Natural Convection
Soldering Profile and Peak Temperature	Wave Flow: 260°C (1.5 mm from case), 10s, max.

Physical Specifications	
Case Material	Black flame-retardant, heat-resistant plastic (UL94 V-O) –
Pin Material	_
Potting Material	_
Weight	1.8g typ.
Case Dimensions	0.46" x 0.29" x 0.40"

EMC Specifications	
Radiated / Conducted Emissions	EN55032 Class B see EMI Filter
ESD	IEC 61000-4-2 Perf.Criteria B
Rad. RF	IEC 61000-4-3 Perf.Criteria A
EFT	IEC 61000-4-4 Perf.Criteria B
Surge	IEC 61000-4-5 Perf.Criteria B
Cond. RF	IEC 61000-4-6 Perf.Criteria A
PFMF	_

The information and specification contained in this data sheet are believed to be correct at time of publication. However RSG accepts no responsibility for consequences arising from printing errors or inaccuracies. Specifications are subject to change without notice.



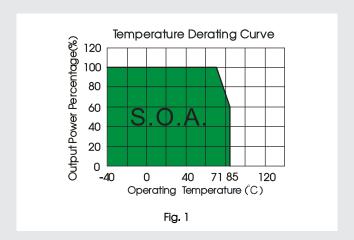
^{†)} These are stress ratings; exposure of devices to any of these conditions may adversely affect long-term reliability.

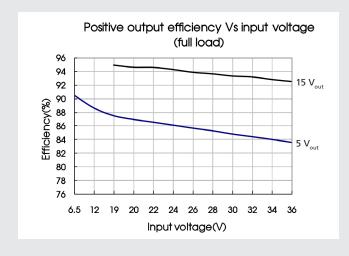
All specifications typical at T_A = 25 °C, nominal input voltage and full load, unless otherwise specified.

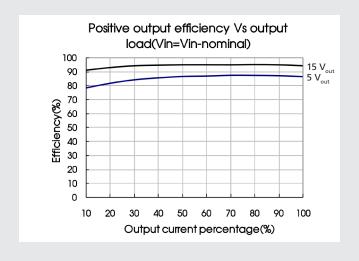
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Number structure RSVR/RTVR Series

RSVR	-	78	F	05	D
Name / package		Compatibility	Usage/Amps	Voltage out	Int. Code
RSVR = SIL3 RTVR = DIL10-SMD		78 = LM78xx	M = Mid-Amp (0.5 A) F = Full-Amp (1.0 A) D = Double-Amp (2.0 A) W = Wide-Input (0.5 A)	00 = 1.5 V 01 = 1.8 V 02 = 2.5 V 15 = 15 V	Logistics Code



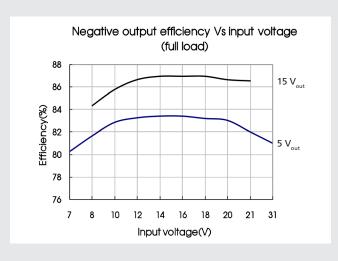


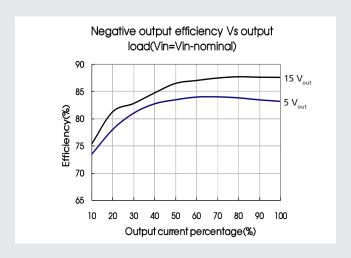


Model Selection Guide

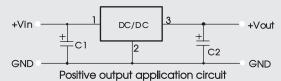
Model Number	Input Voltage (VDC)	Output		Efficiency (%/Typ.)	Max. Capacitive Load (μF)
	Nominal (Range)	Output Voltage (VDC)	Max. Output Current (mA)	(Min. Vin)/ (Max. Vin) @Full Load	
RSVR-78M03v3	24 (4.75~36)	3.3	500	86/80	680
RSVR-78M05v3	24 (6.5~36)	5	500	90/84	680
K3VK-/8IVIU3V3	12 (7~31)	-5	-300	80/81	330
RSVR-78M09v3	24 (12~36)	9	500	93/90	680
DC\/D 70\/122	24 (15~36)	12	500	94/91	680
RSVR-78M12v3	12 (8~24)	-12	-150	84/85	330
DCV/D 70M4E-2	24 (19~36)	15	500	95/93	680
RSVR-78M15v3	12 (8~21)	-15	-150	85/87	330

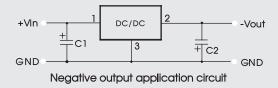
Note: For input voltage higher than 30 VDC, a 22µF/50V input capacitor is required.





Typical Application Circuit





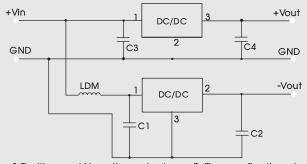
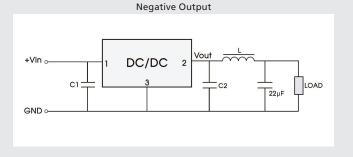


Fig. 3 Positive and Negative output parallelling application circuit

Part No.	C1/C3 (ceramic capacitor)	C2/C4 (ceramic capacitor)
RSVR-78M03v3		22μF/10V
RSVR-78M05v3		22μF/10V
RSVR-78M09v3	10μF/50V	22μF/16V
RSVR-78M12v3		22μF/25V
RSVR-78M15v3		22μF/25V

- 1. C1 and C2 (C3 and C4) are required and should be connected close to the pin terminal of the module.
- 2. For capacitance of C1 and C2 (C3 and C4) refer to table, it can be increased properly if required, and tantalum or low ESR electrolytic capacitors may also suffice.
- 3. When the products used as the circuit like figure 3, an inductor named as LDM up to 10µH is recommended in the circuit to reduce the mutual interference. 4. Cannot be used in parallel for output and hot swap.
- 5. To reduce the output ripple further, it is suggested to connect a "LC" filter at the output terminal, and recommended value of L is 10μH-47μH.

Positive Output DC/DC C1 LOAD 22µF GND o



EMC solution-recommended circuit

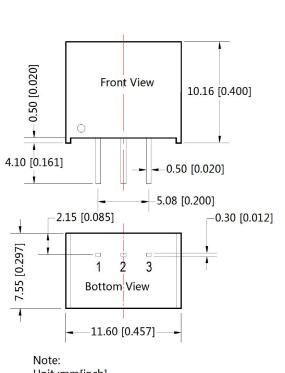
Positive and negative Output Vin FUSE LDM1 LDM2 Vin DC/DC +Vo GND +C5 TC1 C2 LOAD

Parameter description

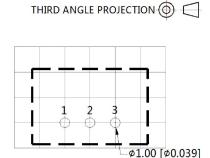
FUSE	Selected based on the actual input current from the customer
MOV	S20K30
LDM1	82µH
C0	680µF/50V
C1/C2	Refer to Sheet 1
C5	4.7μF /50V
LDM2	12µH

Note: Part ① in the Fig. is for EMS test, part ② is for EMI filtering; parts ① and ② can be added based on actual requirement.

Mechanical Specifications



Note: Unit:mm[inch] Pin section tolerances:±0.10[±0.004] General tolerances:±0.25[±0.010]



Note: Grid 2.54*2.54mm

GND

+V Output

Notes

- 1. The maximum capacitive load offered were tested at input voltage range and full load;
- 2. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25 , humidity<75%RH with nominal input voltage and rated output load.

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-V Output

GND



2

3