RTVR-78Mv3

0.5A Non-Isolated, regulated DC/DC Converter





10 Pin (8) SMD Package

■ Wide Input Range

Step-down switching

■ Full SMD Technology

Efficiency up to 95%

■ Operating Temperature Range -40°C ~ +85°C

■ Continuous Short Circuit Protection

Adjustable Output Voltage

Non Conductive Black Plastic Case

Remote On/Off Control

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Output Specifications	
Voltage Accuracy	±2% typ. ±4% max.
Output Voltage Adjustability (Trim)	±10% max.
Maximum Output Current	500mA max.
Line Regulation	±0.2% typ. ±0.4% max.
Load Regulation	from 10% to 100% Load: ± 0.6 % typ. ± 1.0 % max.
Short Circuit Protection	Continuous, self-recovery
Ripple & Noise (20 MHz bandwidth)	20mV typ., 50mV pk-pk max.
Temperature Coefficient	±0.03%/°C
Transient Recovery Time	200μs typ., 1ms max.
Transient Response Deviation	50mV typ., 200mV max.

Input Specifications	
Voltage Range	See table
Start-up Time	-
No-Load/Full-Load Input Current	See table
Input Filter	C/L (see filter details on following pages)
Input Reflected Ripple Current	_
Remote ON	3.2 ~ 8VDC or open circuit
Remote OFF	0 ~ 0.8VDC or short circuit pin 10 and 3/7
OFF Idle Current	0.03mA typ.
Surge Voltage (100 ms) †)	

General Specifications	
Switching Frequency	370kHz ~ 700KHz
Humidity	95% rel H
Reliability Calculated MTBF	>2.0MHrs (MIL-HDBK-217 f)
Safety Standard(s)	IEC/EN62368-1 (designed to meet)

Environmental Specifications						
Operating Temperature Range	-40°C ~ +85°C (see Derating Curve)					
Maximum Case Temperature	_					
Storage Temperature	-55°C ~ +125°C					
Cooling	Natural Convection					
Soldering Profile and Peak Temperature	Pb-free Reflow: 245°C, 10s, max. / 217°C <60s (IPC/JEDEC J-STD-020D.1, MSL 1)					

Physical Specifications	
Case Material	Black flame-retardant, heat-resistant plastic (UL94 V-O) –
Pin Material	-
Potting Material	-
Weight	1.5g typ.
Case Dimensions	0.60" x 0.45" x 0.32"

EMC Specifications	
Radiated / Conducted Emissions	EN55032 Class B see EMI Filter
ESD	IEC 61000-4-2 Perf.Criteria B
Rad. RF	IEC 61000-4-3 Perf.Criteria A
EFT	IEC 61000-4-4 Perf.Criteria B
Surge	IEC 61000-4-5 Perf.Criteria B
Cond. RF	IEC 61000-4-6 Perf.Criteria A
PFMF	_

The information and specification contained in this data sheet are believed to be correct at time of publication. However RSG accepts no responsibility for consequences arising from printing errors or inaccuracies. Specifications are subject to change without notice.



 $^{^{}t)}$ These are stress ratings; exposure of devices to any of these conditions may adversely affect long-term reliability. All specifications typical at $T_A = 25$ °C, nominal input voltage and full load, unless otherwise specified.

Number structure RSVR/RTVR Series

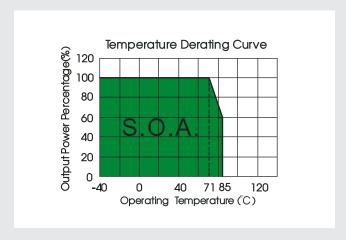
RSVR	-	78	F	05	D
Name / package		Compatibility	Usage/Amps	Voltage out	Int. Code
RSVR = SIL3 RTVR = DIL10-SMD		78 = LM78xx	M = Mid-Amp (0.5 A) F = Full-Amp (1.0 A) D = Double-Amp (2.0 A) W = Wide-Input (0.5 A)	00 = 1.5 V 01 = 1.8 V 02 = 2.5 V 15 = 15 V	Logistics Code

Model Selection Guide

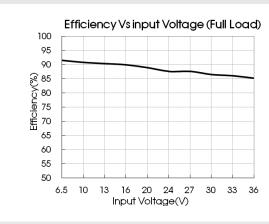
Model Number	Input Voltage (VDC)	Output		Efficiency (%/Typ.)	Max. Capacitive Load (μF)
	Nominal (Range)	Output Voltage (VDC)	Max. Output Current (mA)	(Min. Vin)/ (Max. Vin) @Full Load	
RTVR-78M00Dv3	12 (4.75-28)	1.5	500	76/67	680
RTVR-78M01Dv3	12 (4.75-28)	1.8	500	76/69	680
RTVR-78M02Dv3	12 (4.75-32)	2.5	500	81/74	680
RTVR-78M03Dv3	24 (4.75-36)	3.3	500	86/80	680
RTVR-78M05Dv3	24 (6.5-36)	5	500	90/84	680
RTVR-78M06Dv3	24 (8-36)	6.5	500	92/87	680
RTVR-78M09Dv3	24 (12-36)	9	500	93/90	680
RTVR-78M12Dv3	24 (15-36)	12	500	94/91	680
RTVR-78M15Dv3	24 (19-36)	15	500	95/93	680

Note: For input voltage higher than 30 VDC, a $22\mu\text{F}/50\text{V}$ input capacitor is required.

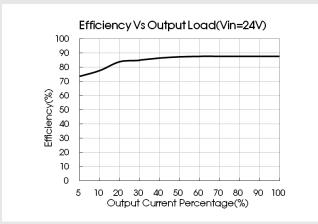
Product Characteristic Curve





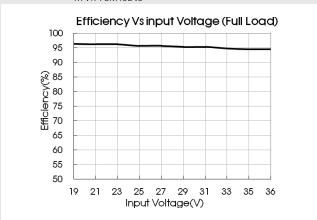


RTVR-78M05Dv3

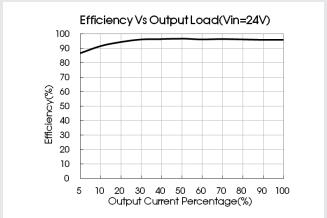


Product Characteristic Curve

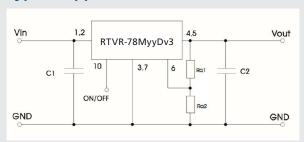
RTVR-78M15Dv3



RTVR-78M15Dv3



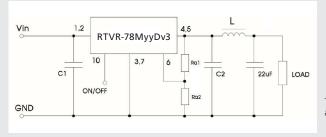
Typical Application Circuit



Part No.	C1 (ceramic capacitor)	C2 (ceramic capacitor)	Ra1/Ra2 (Vadj resistance)
RTVR-78M00Dv3		22µF/10V	
RTVR-78M01Dv3		22µF/10V	
RTVR-78M02Dv3		22µF/10V	
RTVR-78M03Dv3		22µF/10V	Refer to Vadj
RTVR-78M05Dv3	10μF/50V	22µF/16V	resistance
RTVR-78M06Dv3		22µF/16V	calculation
RTVR-78M09Dv3		22µF/25V	
RTVR-78M12Dv3		22µF/25V	
RTVR-78M15Dv3		22μF/25V	

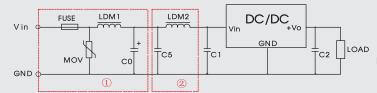
Note:

- 1. C1 and C2 are required and should be connected close to the pin terminal of the module.
- 2. For capacitance of C1 and C2 refer to table, it can be increased properly if required, and tantalum or low ESR electrolytic capacitors may also suffice.
- 3. Cannot be used in parallel for output and hot swap.



To reduce the output ripple furtherly, it is suggested to connect an "LC" filter at the output terminal, and recommended value of L is $10\mu H$ - $47\mu H$.

EMC solution-recommended circuit



Note: Part ${\bf 1}$ in this figure is for EMS test, part ${\bf 2}$ is for EMI filtering; parts ${\bf 1}$ and ${\bf 2}$ can be added based on actual requirement.

FUSE	MOV	LDM1	C0	C1/C2	C5	LDM2
Selected based on the actual input current from the customer	S20K30	82µH	680µF/50V	Refer to Sheet 1	4.7µF /50V	12µH

Application of Vadj and calculation of Vadj resistance

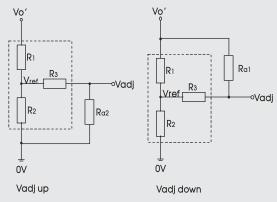


Fig.5 Applied circuits of Vadj (Part in broken line is the interior of models)

Calculation formula of Vadj resistance:

up:
$$Ra2 = \frac{aR2}{R2 - a} - R3$$

$$a = \frac{Vref}{Vo'-Vref}$$
 R1

down:
$$Ra1 = \frac{aR1}{R1 - a} - R3$$

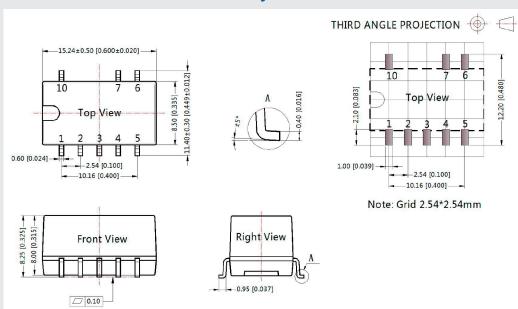
$$a = \frac{\text{Vo'-Vref}}{\text{Vref}} \cdot R$$

Ra1, Ra2 is Vadj resistance, a is a self-defined parameter, with no real meaning. Vo' for the actual needs of the up or down regulated voltage

Vout(V)	R1(KΩ)	R2(K Ω)	R3(KΩ)	Vref(V)
1.5	7.5	7.5	15	0.75
1.8	35.7	26.29	100	0.765
2.5	27	11.858	51	0.765
3.3	33	9.9	47	0.765
5	75	13.5	75	0.765
6.5	75	10	51	0.765
9	51	4.7	27	0.765
12	75	5.1	27	0.765
15	82	4.423	27	0.765

Note: The 1.5VDC output model only supports Vadj up, not down.

Dimensions and Recommended Layout



Note: Unit: mm[inch]

Pin section tolerances: ±0.10[±0.004] General tolerances: ±0.25[±0.010]

Pin	Function
1	+Vin
2	+Vin
3	GND
4	+Vout
5	+Vout
6	V adj
7	GND
10	Remote On/Off

Notes

- 1. The maximum capacitive load offered were tested at input voltage range and full load;
- 2. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25 , humidity<75%RH with nominal input voltage and rated output load.

