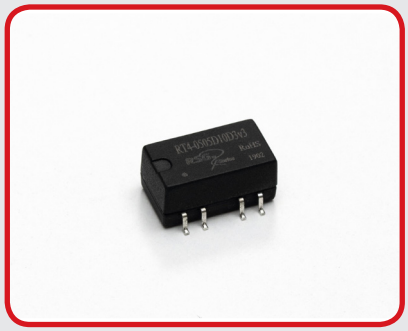
RT3-RS10v3

1.0W Regulated Single Output DC/DC Converter



Picture similar

- **10 Pin (6) SMD Package**
- **±5% Input Range**
- **3000VDC Isolation**
- **EMI Complies with EN55032 Class B**
- **Efficiency up to 71%**
- **Operating Temperature Range -40°C ~ +85°C**
- **Continuous Short Circuit Protection**
- **No-load input current as low as 5mA**
- **Non Conductive Black Plastic Case**

RoHS

Output Specifications	
Voltage Accuracy	±3%, max.
Maximum Output Current	See table
Line Regulation	±0.25% max. (per ±1% Vin Change)
Load Regulation	from 10% to 100% Load: 2% max. Output 3.3V Model: 3%
Cross Regulation (Dual Output)	–
Short Circuit Protection	Continuous, self-recovery
Ripple & Noise (20 MHz bandwidth)	30mV Ripple, 60mV Noise pk-pk typ.
Temperature Coefficient	±0.03%/°C

Input Specifications	
Voltage Range	See table
Start-up Time	–
No-Load/Full-Load Input Current	See table
Input Filter	C/L (see filter details on following pages)
Input Reflected Ripple Current	30mA Typ.
Surge Voltage (100 ms) †)	

General Specifications	
I/O Isolation Voltage (60 sec)	3000VDC
Out1/Out2 Isolation Voltage (Dual Separate)	–
I/O Isolation Capacitance	20pF typ.
I/O Isolation Resistance	1000M Ohm, min
Switching Frequency	250kHz
Humidity	95% rel H
Reliability Calculated MTBF	>3.5MHrs (MIL-HDBK-217 f)
Safety Standard(s)	–

Environmental Specifications	
Operating Temperature range	-40°C ~ +85°C (see Derating Curve)
Maximum Case Temperature	–
Storage Temperature	-55°C ~ +125°C
Cooling	Natural Convection
Soldering Profile and Peak Temperature	Pb-free Reflow: 245°C, 10s, max. / 217°C <60s (IPC/JEDEC J-STD-020D.1, MSL 1)

Physical Specifications	
Case Material	Black flame-retardant heat-proof epoxy resin (UL94 V-O)
Pin Material SIP Case	–
Pin Material DIP Case	–
Potting Material	Epoxy resin (UL94V-V0)
Weight SIP Case	–
Weight DIP Case	1.2g typ.
Dimensions SIP Case	–
Dimensions DIP Case	0.60" x 0.44" x 0.29"

EMC Specifications	
Radiated / Conducted Emissions	EN55032 Class B see EMI Filter
ESD	IEC 61000-4-2 Perf.Criteria B
Rad. RF	–
EFT	–
Surge	–
Cond. RF	–
PFMF	–
VD/SI/VV	–

†) These are stress ratings; exposure of devices to any of these conditions may adversely affect long-term reliability.
All specifications typical at T_A = 25 °C, nominal input voltage and full load, unless otherwise specified.
The information and specification contained in this data sheet are believed to be correct at time of publication. However RSG accepts no responsibility for consequences arising from printing errors or inaccuracies. Specifications are subject to change without notice.

RSG Electronic Components GmbH
Sprendlinger Landstr. 115
D-63069 Offenbach, Germany

Tel. +49 69 984047-0
Fax +49 69 984047-77
info@rsg-electronic.de
www.rsg-electronic.de

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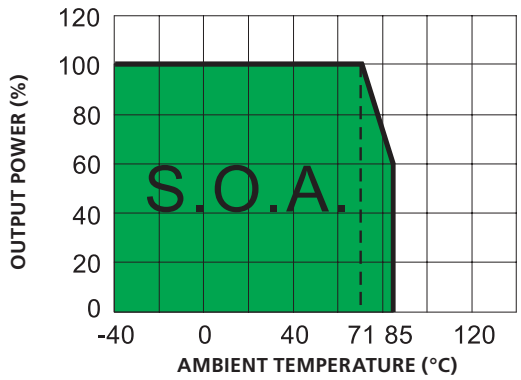
Number structure RT3

RT3	–	05	12	R	S	10	D	3	(v3)
Name / package		**V-input nom.**	**V-output**	**Regulation**	**Output type**	**Power**	**Int. Code**	**Isolation**	
RT3 = SMT-10		05 = 5 V 12 = 12 V … 24 = 24 V	03 = 3.3 V 05 = 5 V … 15 = 15 V	R = regulated	S = Single	10 = 1.00 W	Logistics Code	1 = 1.5 kVDC 3 = 3.0 kVDC	

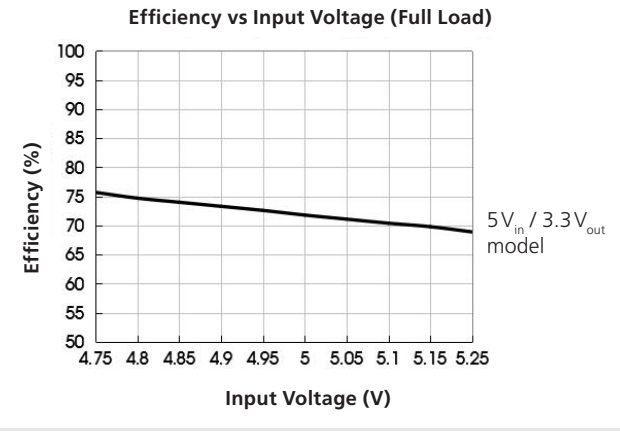
Derating Curve

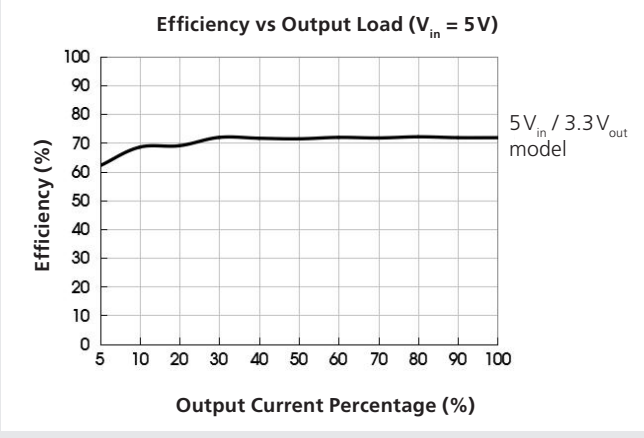


Model Selection Guide

Model Number	Input		Output		Efficiency	Capacitor Load (µF)
	Voltage (VDC) Nom. (Range)	max. Current (mA) full/no load	Voltage (V DC)	Current (mA) max./min.	@ Full Load (%, Min./Typ.)	max.
RT3-0503RS10D3v3	5 (4.75~5.25)	323/10	3.3	250/25	62/66	2400
RT3-0505RS10D3v3	5 (4.75~5.25)	308/10	5	200/20	65/69	2400
RT3-0509RS10D3v3	5 (4.75~5.25)	304/20	9	111/12	66/70	1000
RT3-0512RS10D3v3	5 (4.75~5.25)	299/30	12	84/9	67/71	560
RT3-0515RS10D3v3	5 (4.75~5.25)	299/30	15	67/7	67/71	560

Electrical Characteristic Curves





The models listed above are standard types. If you need special specifications or have questions regarding packing or need application support, please contact our specialists:
sales@rsg-electronic.de or +49 69-984047-0

Design Reference

1. Typical application circuit

Input and/or output ripple can be further reduced by connecting a filter capacitor from the input and/or output terminals to ground as shown in Fig.2. Choosing suitable filter capacitor values is very important for a smooth operation of the modules, particularly to avoid start-up problems caused by capacitor values that are too high. For recommended input and output capacitor values refer to Table 1.

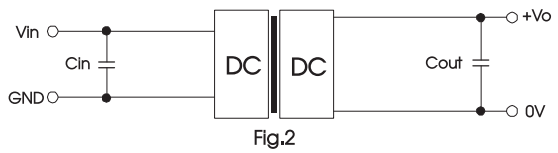


Fig.2

Recommended capacitive load value table (Table 1)			
V_{in} (VDC)	C_{in} (µF)	V_o (VDC)	C_{out} (µF)
5	4.7	3.3 / 5	10
		9 / 12	2.2
		15	1

2. EMC compliance circuit

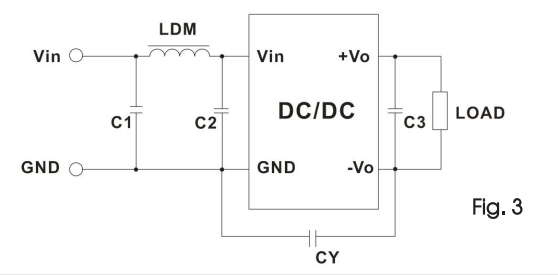
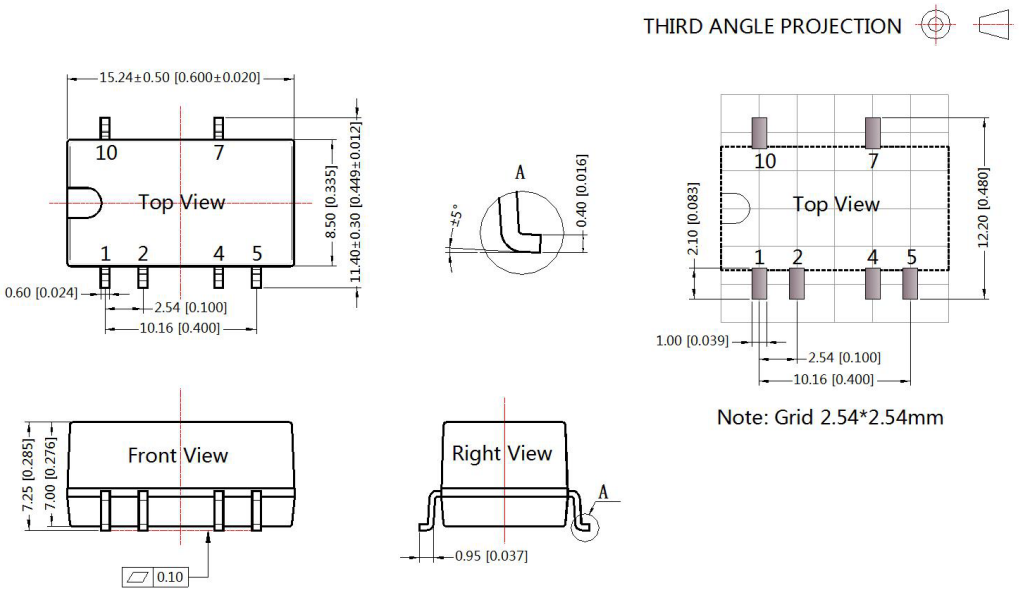


Fig. 3

EMC recommended circuit value table (Table 2)			
Input voltage	Output voltage	3.3/5/9	12/15
5 VDC	EMI	C1/C2	4.7 µF/25 V
		CY	– 1 nF/4 kVDC VISHAY HGZ102MBP TDK CD45-E2GA102M-GKA
		C3	Refer to the C_{out} in table 1
		LDM	6.8 µH

Mechanical Specifications



Note: Grid 2.54*2.54mm

Pin	
1	GND
2	V Input
4	0V
5	0V
7	+V Output
10	N C

NC: Pin to be isolated from circuitry

Note:
Unit: mm[inch]
Pin section tolerances: ±0.10[±0.004]
General tolerances: ±0.25[±0.010]

Notes

1. If the product is not operated within the required load range, the product performance cannot be guaranteed to comply with all parameters in the datasheet.
2. The maximum capacitive loads offered were tested at input voltage range and full load.
3. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta = 25, humidity < 75 %RH with nominal input voltage and rated output load.

RSG Electronic Components GmbH
Sprendlinger Landstr. 115
D-63069 Offenbach, Germany

Tel. +49 69 984047-0
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